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## Calculating Square Root of Binary Numbers With Fixed-Point Microprocessor

### **Abstract of the Disclosure**

A method and system is disclosed for calculating the square root of a number using a fixed-point microprocessor. The method includes employing a binary search to obtain the integer portion of the square root, and calculating a fraction of the square root utilizing the integer portion. The method further includes summing the fractional portion together with the integer portion to yield the square root. Also disclosed is a calculator apparatus for employing the method.